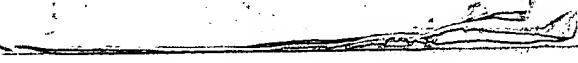
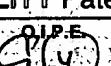
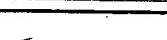
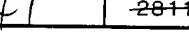
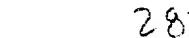


		Subclass			PATENT NUMBER	
		Class				
				ISSUE CLASSIFICATION		
						
<b>U.S. UTILITY Patent Application</b>						
 SCANNED			 GPO	 Q.A.	PATENT DATE	
APPLICATION NO.		CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/696813			257	347	2826 2811	 
APPLICANTS	 Apostolos Voutsas  Yukihiko Nakata  Takeshi Hosoda					
TITLE	   semiconductor device and a method of creating the same utilizing metal induced crystallization while suppressing partial solid phase crystallization					
PTO-2040 12/89						

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	Sheet(s) Drwg.	Fig(s). Drwg.	Print. Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	(Assistant Examiner)	(Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner)	(Date)		<b>ISSUE FEE</b>	
	(Agent/Instruments Examiner)	(Date)		<b>ISSUE BATCH NUMBER</b>	

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